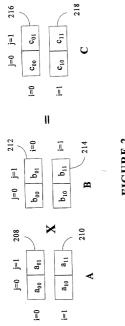
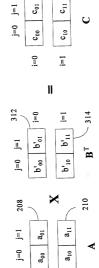


FIGURE 1







<u>=0</u>

Ξ

218

FIGURE

APPLIN, FILING-DATE, MARCH 21, 2001, TITLE: MATRIX MULTIPLICATION HYB. WEEGOR PROCESSING SYSTEM INVENORES: ALL SAZEGAU APPLICATION SERIAL NO: UNASSIGNED SHEET 3 of 15

j=1 j=2 j=3 424	C ₀₁ C ₀₂ C ₀₃ 426	c ₁₁ c ₁₂ c ₁₃ 428	c ₂₁ c ₂₂ c ₂₃ 430	C ₃₁ C ₃₂ C ₃₃	£
j=0 j=	o oo	010	C ₂₀	C ₃₀ C	
[i=0	Ī	i=2	i=3	
416	418	3 420 ==	422		
j=2 j=3	b ₀₂ b ₀₃	b ₁₂ b ₁₃	b ₂₂ b ₂₃	b ₃₂ b ₃₃	
Ϊ	b ₀₁	b ₁₁	b ₂₁	b ₃₁	<u> </u>
j=0	p ₀₀	b ₁₀	b ₂₀	b ₃₀	
	<u>=</u>	<u></u>	<u>i=</u>	<u>=</u> 3	
408 410 7 4112 X					
408	1				
. = 3	B	a ₁₃	a ₂₃	a ₃₃	
j=2	a ₀₂	a ₁₂	a ₂₂	a ₃₂	
Ξ.	a ₀₁	a ₁₁	a ₂₁	a ₃₁	
j=0	a ₀₀	a ₁₀	a ₂₀	a ³⁰	

0=I

<u>i=1</u>

i=3

FIGURE 4

APPLN. FILING DATE: MARCH 21, 2001

TITLE: 'MATRIX MULTIPLICATION IN A VECTOR PROCESSING SYSTEM

INVENTOR(S): ALI SAZEGARI

APPLICATION SERIAL NO: UNASSIGNED SI

SHEET 4 of 15

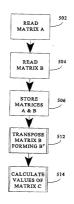


FIGURE 5

APPLA. FILING DATE: MARCH 21, 2001
THILE: MATRIX MULTIPLICATION IN A VECTOR
PROCESSING SYSTEM
INVENTOR(S): ALI SAZEGARI
APPLICATION SERIAL NO: UNASSIGNED
SHEET 5 of 15

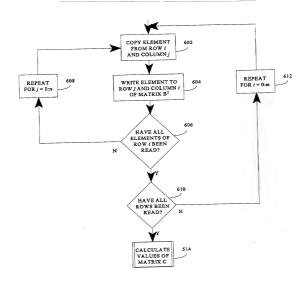


FIGURE 6

. APPLN. FILING DATE: MARCH 21, 2001
TITLE: MATRIX MULTIPLICATION IN A VECTOR
PROCESSING SYSTEM

INVENTOR(S): ALI SAZEGARI

APPLICATION SERIAL NO: UNASSIGNED SI

SHEET 6 of 15

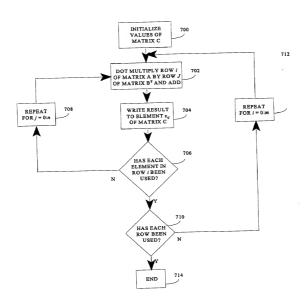


FIGURE 7

* APPLN. FILING DATE: MARCH 21, 2001
TITLE: MATRIX MULTIPLICATION IN A VECTOR
PROCESSING SYSTEM
INVENTOR(S): ALI SAZEGARI
APPLICATION SERIAL NO: UNASSIGNED
SHEET 7 of 15

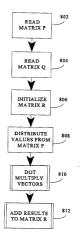


FIGURE 8A

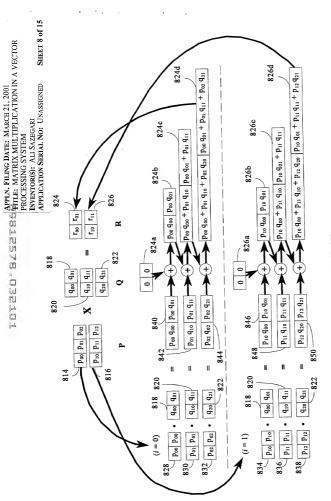


FIGURE 8B

APPLN. FILING DATE: MARCH 21, 2001 TITLE: MATRIX MULTIPLICATION IN A VECTOR PROCESSING SYSTEM INVENTOR(S): ALI SAZEGARI

APPLICATION SERIAL NO: UNASSIGNED SHEET 9 of 15

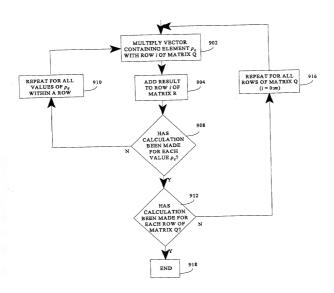


FIGURE 9

AAPUN, FULNG DATE: MARCH 21, 2001
TITUE: MATERIKAMHETIPLIGATIONAN WEGEOR
PROCESSING SYSTEM
PROCESSING SYSTEM
APPLICATION SERRAL NO: UNASSIGNED
SHEET 10 of 15

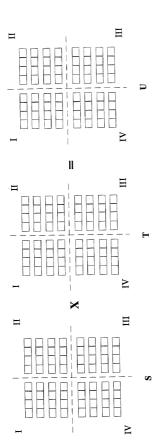


FIGURE 10

1142 1144 Ξ 1138 Z₀₂ Z₀₃ Z_{23} Z_{33} Z₁₃ Z22 Z₃₂ Z12 7 \mathbf{Z}_{20} \mathbf{Z}_{21} Z₃₀ Z₃₁ Z10 Z11 z_{01} Z00 1148 1134 1146 1136 SHEET 11 of 15 II APPLN. FILING DATE: MARCH 21, 2001. TITLE: MATRIX MULTIPLICATION IN A VECTOR 1128 Ξ 1122 y₃₂ | y₃₃ y22 y23 yo2 yo3 y12 | y13 | APPLICATION SERIAL NO: UNASSIGNED INVENTOR(S): ALI SAZEGARI y 00 y 01 y10 y11 y 20 y 21 y30 y31 PROCESSING SYSTEM 1132 1118 1130 1120 \geq i 1108 1116 1106 Ε X32 X33 X22 X23 X₀₂ X₀₃ X12 X13 X30 X31 X₀₀ X₀₁ X20 X21 X10 X11 1116 1102 1114 1104 \geq

FIGURE 11

×

APPLA, FILING DATE-TABLICATION IN A VECTOR TITLE: MATRIX MULTIPLICATION IN A VECTOR PROCESSING SYSTEM INVENTORIS, ALL SAZEGARI SPRIAL NO: UNASSIGNED SHEET 12 of 15

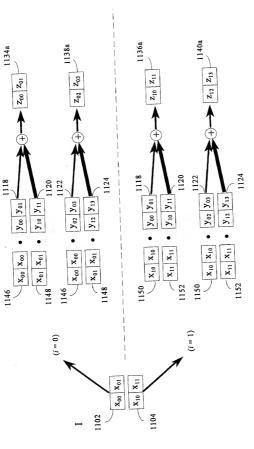


FIGURE 11A

1134b Z₁₂ Z₁₃ SHEET 13 of 15 APPLN. FILING DATE: MARCH 21, 2001 TITLE: MATRIXIMULE PLICATION IN A VEGTOR 1128 y₃₂ y₃₃ y₂₀ y₂₁ y₃₀ y₃₁ y22 y23 y₃₂ y₃₃ y₃₀ y₃₁ y₂₂ y₂₃ y20 y21 APPLICATION SERIAL NO: UNASSIGNED PROCESSING SYSTEM INVENTOR(S): ALI SAZEGARI X13 X13 X₀₂ X₀₂ X12 X12 X₁₃ X₁₃ X₁₂ X₁₂ X₀₃ X₀₃ X₀₂ X₀₂ X₀₃ X₀₃ 1156 1160 1160 1158 (i = 0)(i = 1)X13 X 02 X 12 1106

FIGURE 11B

1128

APPLN. FILING DATE: MARCH 21, 2001
TITLE: MATRIX MULTIPLEGATION IN ASTECTIOR:
PROCESSING SYSTEM
INVERTOR(S). ALL SASEGARI
APPLICATION SERIAL NO: UNASSIGNED
SHEEF 14 of 15

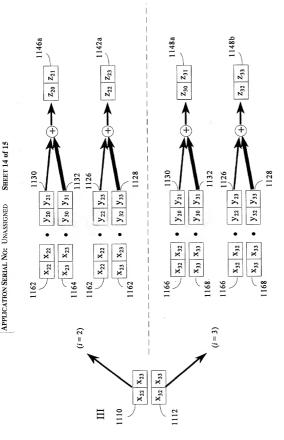


FIGURE 11C

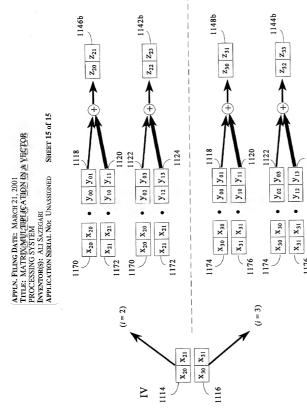


FIGURE 11D

1124